Beta Reduction is Invariant, Indeed

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Abstract

Slot and van Emde Boas’ invariance thesis states that reasonable machines can simulate each other within a polynomially overhead in time. Is λ-calculus a reasonable machine? Is there a way to measure the computational complexity of a λ-term? This paper presents the first complete positive answer to this long-standing problem. Moreover, our answer is completely machine-independent and based over a standard notion in the theory of λ-calculus: the invariant cost model is the length of a leftmost-outermost derivation to normal form.

1 Introduction

When doing complexity analysis, one expects a neat relationship between primitives of the model and the way in which they are effectively implemented. In this respect, random access machines are often taken as the reference model, since their definition closely reflects the von Neumann architecture. The specifications of algorithms unfortunately lies on the other end of the spectrum, as one would like to be as machine-independent as possible. In this case the typical model is provided by programming languages. Functional programming languages, thanks to their higher-order nature, provide very concise and abstract specification models. Their strength is of course also their drawback: the abstraction from physical machines is pushed to a level where it is no longer clear how to measure the complexity of an algorithm. Is there a way in which such a tension can be solved?

The tools for stating the question formally are provided by complexity theory and by Slot and van Emde Boas invariance thesis [9]. Two computational models are invariant if they can simulate each other so as to make complexity classes independent from the model on top of which they are defined. Ideally, one would like only a linear overhead in the simulation. This is usually a too strong requirement, so that a polynomial invariance — that preserves the (non-)polynomial character of algorithms and computational problems — is instead considered. So a first refinement of our question is: are functional languages polynomially invariant with respect to standard models like random access machines or Turing machines? Such an invariance has to be proved via an appropriate measure of time complexity for programs, i.e. a cost model.

The natural answer is to take the unitary cost model, i.e. the number of evaluation steps. However, this is not well-defined. The evaluation of functional programs, indeed, depends very much on the evaluation strategy chosen to implement the language, as the λ-calculus — the reference model for functional languages — is so machine-independent that it does not even come with a deterministic evaluation strategy. And which strategy, if any, gives us the most natural, or canonical cost model (whatever that means)? These questions have received some attention in the last decades. The number of optimal parallel β-steps (in the sense of Lévy [8]) to normal form has been shown not to be a reasonable cost model: there exists a family of terms which reduces in a polynomial number of parallel β-steps, but whose complexity is non-elementary [7, 3]. If one considers the number of sequential β-steps (in a given strategy, for a given notion of reduction), the literature contains some partial positive results, all relying on the use of sharing (see below for more details). Some quite general results [6, 4] have been obtained through graph rewriting, itself a form of sharing, when only first order symbols are considered.

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Sharing is indeed a key ingredient, for one of the issues here is due to the representation of terms. But is appropriately managed sharing enough? The literature offers some positive, but partial, answers to this question. The number of steps is indeed known to be an invariant cost model for weak reduction \([5, 6]\) and for head reduction \([2]\).

If the problem at hand consists in computing the normal form of an arbitrary \(\lambda\)-term, however, no positive answer is known. We believe that it is embarrassing for the \(\lambda\)-calculus community not knowing whether the \(\lambda\)-calculus in its full generality is a reasonable machine. In addition, this problem is relevant in practice: proof assistants often need to check whether two terms are convertible, itself a problem that can be reduced to the one under consideration.

In this work, we give a positive answer to the question above, by showing that leftmost-outermost reduction to normal form indeed induces an invariant cost-model.

As in our previous work \([2]\), we prove our result by means of the linear substitution calculus, a simple calculus of explicit substitutions based on a new at a distance approach, arising from linear logic and graphical syntaxes. Such a framework allows an easy management of sharing and, in contrast to previous approaches to explicit substitution, it admits a theory of standardization \([1]\). Indeed, the proof passes through a fine quantitative study of the relationship between standard reductions for the \(\lambda\)-calculus and some special standard reductions for the linear substitution calculus. Roughly, the latter avoids the size explosion problem while keeping a polynomial relationship with the former.

## 2 Why is The Problem Hard?

In principle, one may wonder why sharing is needed at all, or whether a relatively simple form of sharing suffices. In this section, we will show why this is not the case.

If we stick to explicit representations of terms, in which sharing is not allowed, counterexamples to invariance can be designed in a fairly easy way. Consider the sequence of \(\lambda\)-terms defined as follows, by induction on a natural number \(n\) (where \(u\) is the lambda term \(yxx\)): \(t_0 = u\) and for every \(n \in \mathbb{N}\), \(t_{n+1} = (\lambda x.t_n)u\). The term \(t_n\) has size linear in \(n\), and \(t_n\) rewrites to its normal form \(r_n\) in exactly \(n\) steps, following the leftmost-outermost reduction order; as an example:

\[
\begin{align*}
t_0 & \equiv u \equiv r_0; \\
t_1 & \rightarrow yuu \equiv yr_0r_0 \equiv r_1; \\
t_2 & \rightarrow (\lambda x.t_0)(yu u) \equiv (\lambda x.u)r_1 \rightarrow yr_1r_1 \equiv r_2.
\end{align*}
\]

For every \(n\), however, \(r_{n+1}\) contains two copies of \(r_n\), hence the size of \(r_n\) is exponential in \(n\). As a consequence, the unitary cost model is not invariant: in a linear number of \(\beta\)-steps we reach an object which cannot even be written down in polynomial time.

The solution the authors proposed in \([2]\) is based on explicit substitutions, and allows to tame the size explosion problem in a satisfactory way when head-reduction suffices. In particular, the head steps above become the following linear head steps:

\[
\begin{align*}
t_0 & \equiv u \equiv p_0; \\
t_1 & \rightarrow (yxx)[x/u] \equiv u[x/u] \equiv p_1; \\
t_2 & \rightarrow ((\lambda x.t_0)u)[x/u] \equiv ((\lambda x.u)u)[x/u] \rightarrow u[x/u][x/u] \equiv p_2.
\end{align*}
\]

As one can easily verify, the size of \(p_n\) is linear in \(n\). More generally, linear head reduction has the subterm property, i.e. it only duplicates subterms of the initial term. This fact implies that the size of the result and the length of the derivation are linearly related. In other words, the size explosion problem has been solved. Of course one needs to show that 1) the compact results unfold to the expected result (that may be exponentially bigger), and 2) that compact representations can be managed efficiently (typically they can be tested for equality in time polynomial in the size of the compact representation), see \([2]\) for more details.

It may seem that one is then forced to use ES to measure complexity. In \([2]\) we also showed that LHR is at most quadratically longer than head reduction, so that the polynomial invariance
of LHR lifts to head reduction. This is how we exploit sharing to circumvent the size explosion problem: we are allowed to take the length of the head derivation as a cost model, even if it suffers of the size explosion problem, because the actual implementation is meant to be via LHR.

There is a natural candidate for extending the approach to reduction to normal form: just iterate the (linear) head strategy on the arguments, obtaining the (linear) LO strategy, that does compute normal forms [1]. As we will show, for linear LO derivations the subterm property holds. The size of the output is still under control, being linearly related to the length of the LO derivation. Unfortunately, when computing normal forms this is not enough.

One of the key points in our previous work was that there is a notion of linear head normal form that is a compact representation for head normal forms. The generalisation of such an approach to normal forms has to face a fundamental problem: what is a linear normal form? Indeed, terms with and without ES share the same notion of normal form. Consider again the family of terms \( \{ t_n \}_{n \in \mathbb{N}} \): if we go on and unfold all ES in \( p_n \), we end up in \( r_n \). Thus, by the subterm property, the linear LO strategy takes an exponential number of steps, and so it cannot be polynomially invariant with respect to the LO strategy.

Summing up, we need a strategy that 1) implements the LO strategy, 2) has the subterm property and 3) never performs useless substitution steps, i.e. those steps whose role is simply to explicit the normal form, without contributing in any way to \( \beta \)-redexes. The main contribution of this work is the definition of such a linear useful strategy, and the proof that it is indeed polynomially invariant with respect to both the LO strategy and a concrete implementation model.

This is not a trivial task, actually. One may think that it is enough to evaluate a term \( t \) in a LO way, stopping as soon as the unfolding \( u \) of the current term \( u \) — i.e. the term obtained by expanding the ES of \( u \) — is a \( \beta \)-normal form. Unfortunately, this simple approach does not work, because the exponential blow-up may be caused by ESs lying between two \( \beta \)-redexes, so that proceeding in a LO way would execute the problematic ES any way.

Our notion of useful step will elaborate on this idea, by calculating partial unfoldings, to check if a substitution step contributes or will contribute to some \( \beta \)-redex. Of course, we will have to show that such tests do not cause another exponential blow up and that the notion of LO useful reduction retains all the good properties of LO reduction.

3 The Calculus

We assume familiarity with the \( \lambda \)-calculus. The language of the linear substitution calculus (LSC for short) is given by the following grammar for terms:

\[
t, u, r, p ::= x \mid \lambda x . t \mid tu \mid t[x := u]
\]

The constructor \( t[x := u] \) is called an explicit substitution (of \( u \) for \( x \) in \( t \)), the usual (implicit) substitution is instead noted \( t[x := u] \). Both \( \lambda x . t \) and \( t[x := u] \) bind \( x \) in \( t \).

The operational semantics of the LSC is defined using contexts. For the scope of the paper it is enough to use shallow contexts, simply referred as context and defined as (note the absence of the production \( t[x := S] \)):

\[
S, P, T ::= \langle \cdot \rangle \mid \lambda x . S \mid St \mid tS \mid S[x := t]
\]

A special class of contexts is that of substitution contexts, defined by \( L ::= \langle \cdot \rangle \mid L[x := t] \). The (shallow) rewriting rules \( \rightarrow_{\text{dB}} \) (dB = \( \beta \) at a distance) and \( \rightarrow_{\text{lS}} \) (linear substitution) are given by the closure by (shallow) contexts of the following rules:

\[
L(\lambda x . t) u \rightarrow_{\text{dB}} L(t[x := u]); \quad S(x)[x := u] \rightarrow_{\text{lS}} S(u)[x := u].
\]

The union of \( \rightarrow_{\text{dB}} \) and \( \rightarrow_{\text{lS}} \) is simply noted \( \rightarrow \). Taking the external context into account, a substitution step takes the following explicit form: \( P(S(x)[x := u]) \rightarrow_{\text{lS}} P(S(u)[x := u]) \). We shall often use a compact form, writing \( T(x) \rightarrow_{\text{lS}} T(u) \) where it is implicitly assumed that \( T = P(S[x := u]) \). A derivation \( \rho \) is a finite sequence of reduction steps. We write \( |t| \) for the size of \( t \), \( |\rho|_T \) for the number of substitutions in \( t \), \( |\rho|_P \) for the length of \( \rho \), and \( |\rho|_dB \) for the number of dB-steps in \( \rho \).
(Relative) unfolding. The unfolding \( t \downarrow \) of a term \( t \) is the \( \lambda \)-term obtained from \( t \) by turning its explicit substitutions in implicit ones:

\[
x \downarrow := x; \quad (tu) \downarrow := t[u]_\downarrow; \quad (\lambda x.t) \downarrow := \lambda x.t_\downarrow; \quad (t[x\leftarrow u]) \downarrow := t[x\leftarrow u_\downarrow].
\]

We will also need a more general notion, the unfolding \( t \downarrow S \) of \( t \) in a context \( S \):

\[
t \downarrow S := t \downarrow; \quad t \downarrow uS = t \downarrow uS \lambda x, t := t \downarrow S; \quad t \downarrow S[x\leftarrow u] := t \downarrow S[x\leftarrow u_\downarrow].
\]

4 A Bird’s Eye View on the Main Result

Our proof method can be described abstractly. We want to show that a certain strategy \( \rightarrow \) of \( \lambda \)-calculus (namely the leftmost-outermost strategy) provides a unitary and invariant cost model, i.e. that the number of \( \rightarrow \) steps is a measure polynomially related to the number of transition on Turing machines. This is done by going through an intermediary computational model, a calculus with explicit substitutions, the LSC, playing the role of a very abstract machine for Turing machines. This is done by going through an intermediary computational model, a calculus with explicit substitutions, the LSC, playing the role of a very abstract machine for Turing machines. Our target result then decomposes in two parts:

1. **High-Level Implementation**: \( \rightarrow \) terminates iff \( \rightarrow_X \) terminates. Moreover, \( \rightarrow \) is implemented by \( \rightarrow_X \) with only a polynomial overhead. Namely, \( t \rightarrow_X^k u \) iff \( t \rightarrow^h u_\downarrow \) with \( k \) polynomial in \( h \) (our actual bound will be quadratic);

2. **Low-Level Implementation**: \( \rightarrow_X \) is implemented on Turing machines with an overhead in time which is polynomial in both \( k \) and the size of \( t \).

The high-level part relies on the following notion.

**Definition 4.1.** Let \( \rightarrow \) be a deterministic strategy on \( \lambda \)-terms and \( \rightarrow_X \) a strategy on terms with ES. The pair \((\rightarrow, \rightarrow_X)\) is a **high-level implementation system** if whenever \( t \) is a \( \lambda \)-term and \( \rho : t \rightarrow_X^* u \) then:

1. Normal Form: if \( t \) is a \( \rightarrow_X \)-normal form then \( t \downarrow \) is a \( \rightarrow \)-normal form.
2. Projection: \( \rho \downarrow : t \rightarrow^* u_\downarrow \) and \( |\rho| = |\rho|_\text{db} \).
3. Trace: the number \( |u|_{\downarrow} \) of ES in \( u \) is exactly the number \( |\rho|_\text{db} \) of \( \text{db} \)-steps in \( \rho \);
4. Syntactic Bound: the length of a sequence of substitution steps from \( u \) is bounded by \( |u|_{\downarrow} \).

Then we can prove abstractly the high-level implementation theorem.

**Theorem 4.2 (High-Level Implementation).** Let \( t \) be an ordinary \( \lambda \)-term and \((\rightarrow, \rightarrow_X)\) a high-level implementation system. Then:

1. \( t \) is \( \rightarrow \)-normalising iff it is \( \rightarrow_X \)-normalising,
2. If \( \rho : t \rightarrow_X^* u \) then \( \rho \downarrow : t \rightarrow^* u_\downarrow \) and \( |\rho| = O(|\rho|^2) \).

The low-level part, instead, follows from the following notion.

**Definition 4.3.** A strategy \( \rightarrow_X \) on terms with explicit substitutions is mechanisable if for every derivation \( \rho : t \rightarrow_X^* u \):

1. Subterm: all the subterms duplicated along \( \rho \) are subterms of \( t \).
2. Selection: the search of the next \( \rightarrow_X \) redex to reduce in \( u \) takes polynomial time in \( |u| \).

Let us point out that the subterm property implies the trace property in the definition of high-level implementation systems, when the starting term is a \( \lambda \)-term. Indeed, it implies that substitution steps do not change \(|\cdot|_\downarrow\), as all duplicated subterms are without explicit substitutions.

At first sight the selection property is always trivially verified: finding a redex in \( u \) is certainly linear in \( |u| \). However — as it will be explained in a moment — our strategy for ES will reduce only redexes satisfying a side-condition whose naive verification is exponential in \( |u| \). Then one has to be sure that such a computation can be done in polynomial time.

**Theorem 4.4 (Low-Level Implementation).** Let \( \rightarrow_X \) be a mechanisable strategy and let \( t \rightarrow_X^k u \). Then there is an algorithm of input \( t \) and output \( u \) that is polynomial in \( k \) and \( |t| \).
Our strategy for $\beta$-reduction is the leftmost-outermost (LO) strategy $\rightarrow_{LO}$, that is standard. What is left to do, then, is to find the strategy $\rightarrow_X$ for explicit substitutions, which is both mechanisable and a high-level implementation of $\rightarrow_{LO}$. Unfortunately, the linear LO strategy $\rightarrow_{LO}$ (defined in [1]) is mechanisable but the pair $(\rightarrow_{LO}, \rightarrow_{LO})$ does not form a high-level implementation system, since $\rightarrow_{LO}$ lacks the syntactic bound property. (Just consider the family $\{t_n\}_{n \in \mathbb{N}}$ of terms that evaluate in a linear number of $\rightarrow_{LO}$ steps to an output $u_n$ of size exponential in $n$.)

The key technical contribution of this work is the definition of a constrained, optimized notion of reduction, that will pave the way to the High-Level Implementation Theorem without sacrificing the Low-Level Implementation Theorem. The idea is that an optimised step takes place only if it contributes somehow to explicit a $\rightarrow_{dB}$-redex. Let an applicative context be defined by $A ::= (\cdot)u | S\langle \cdot \rangle$ (remember that $S$ denotes a shallow context). Then:

**Definition 4.5** (Useful Steps and Derivations). An *useful* step is either a $\text{dB-step}$ or a $\text{ls-step}$ $S(x) \rightarrow_{ls} S(r)$ (in compact form) s.t. $r_{ls}$:
1. contains a $\beta$-redex, or
2. it is an abstraction and $S$ is an applicative context.

A *useful derivation* is a derivation whose steps are useful.

The notion of small-step evaluation that we will use to implement LO $\beta$-reduction is the one of LO useful (LOU) derivation. This is defined by just imposing that at any step the fired redex is the LO among all useful redexes. In LO derivations, on the other hand, we are allowed to fire the LO redex, even if the latter is not useful in the sense of Definition 4.5.

We need to ensure that LOU derivations are mechanisable and form an high-level implementation system when paired with LO $\beta$-derivations. The proof of these two facts is a sophisticated detour in four steps:
1. The *subterm* and *trace properties*, by showing that:
   - standard derivations for the LSC (introduced in [1]) have the subterm property;
   - LO derivations are standard;
2. The *normal form* and *projection properties*, by a careful study of unfoldings, LO, and LOU derivations;
3. The *syntactic bound property*, by:
   - introducing the new notion of nested derivations and show that they satisfy the property;
   - showing that LOU derivations are nested;
4. The *selection* property, by exhibiting an algorithm to test whether a redex is useful or not, which works in time polynomial on the size of the underlying (compressed) term.

Unfortunately, lack of space prevents us from giving any detail on the four steps above.

**References**